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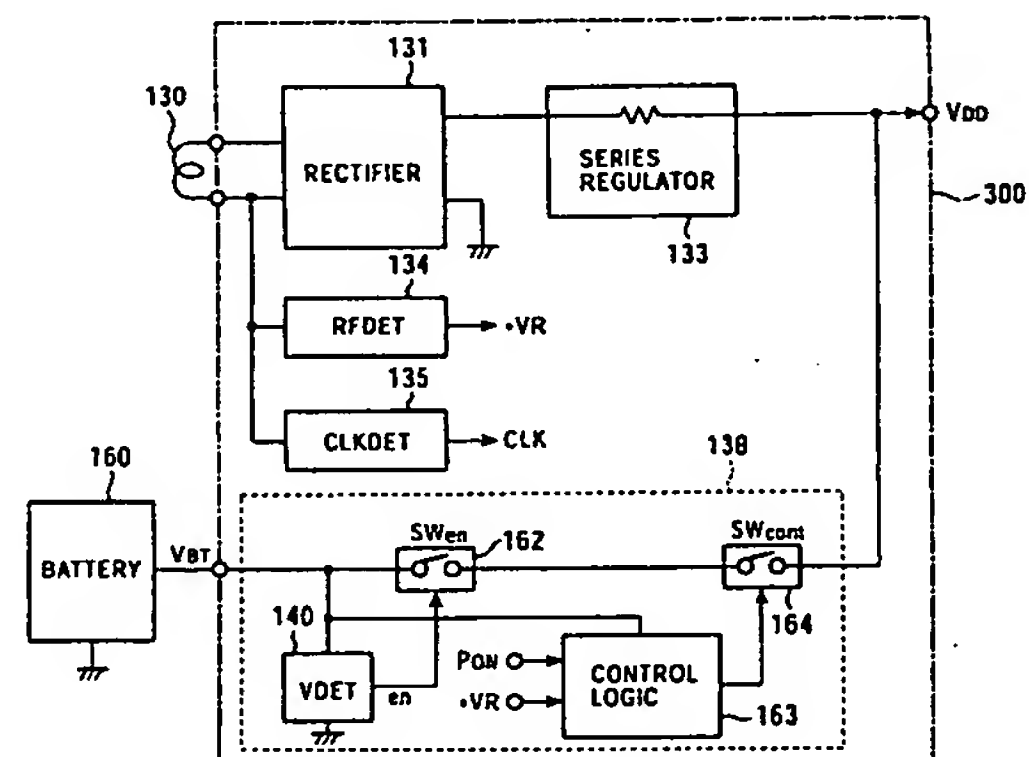
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(54) **CHIP FOR NONCONTACT READER/WRITER HAVING FUNCTION FOR MANAGING POWER  
SUPPLY**

(57) The present invention relates to a semiconductor integrated-circuit device capable of seamlessly switching a power source obtained by rectifying a carrier wave and an external power source. The semiconductor integrated-circuit device having a non-contact card function and a non-contact reader/writer function includes a rectifier 131 for rectifying a received carrier wave, a series regulator 132 for obtaining a predetermined voltage from an output voltage of the rectifier 131, and a power-supply control circuit 138 for turning on/off the voltage from a battery 160. In a case where the output voltage of the battery 160 is equal to or higher than a predetermined voltage, the power-supply control circuit 138 selects the voltage of the battery 160 as power required for operation of an IC 300 when a reader/writer mode signal or a card mode signal is received. On the other hand, when the output voltage of the battery 160 is lower than the predetermined voltage, the power-supply control circuit 138 selects the output voltage from the rectifier 131 as power required for operation of the IC 300. The present invention can be applied to a portable terminal, such as a cellular phone.

FIG. 5



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## Description

### Technical Field

**[0001]** The present invention relates to a semiconductor integrated-circuit device. More particularly, the present invention relates to a semiconductor integrated-circuit device having incorporated therein analog front-ends for use with a data carrier and/or for use with an information processing apparatus for an external data carrier.

### Background Art

**[0002]** A method in which a non-contact IC card is introduced into, for example, a railroad ticket gate system and is used when passing through the ticket gate has begun to be put into practical use. Fig. 10 shows an overall configuration of a non-contact IC and a reader/writer device for reading/writing this non-contact IC card. In Fig. 10, reference numeral 200 denotes a reader/writer (R/W) device, and reference numeral 300 denotes a non-contact IC card. Reference numeral 201 denotes a modem, reference numeral 202 denotes a CPU, reference numeral 203 denotes an oscillator, and reference numeral 204 denotes an antenna. Reference numeral 301 denotes an antenna, reference numeral 310 denotes a rectifier, reference numeral 312 denotes a diode, reference numeral 313 denotes a capacitor, reference numeral 320 denotes a modulator, reference numeral 322 denotes an impedance device, reference numeral 323 denotes an FET diode, reference numeral 330 denotes a high-pass filter (HPF), reference numeral 331 denotes a capacitor, reference numeral 332 denotes a resistor, reference numeral 340 denotes a regulator, reference numeral 350 denotes a demodulator, reference numeral 360 denotes a sequencer, and reference numeral 370 denotes a memory.

**[0003]** The R/W device 200 and the IC card 300 of Fig. 10 form a non-contact IC card system in which data is transmitted and received to and from each other via each antenna. In such a non-contact IC card system, the non-contact IC card 300 rectifies a carrier wave modulated in accordance with transmission data from the R/W device 200 in order to generate a DC voltage, and supplies the voltage as power for the internal CPU and circuits such as the memory. Specifically, the voltage rectified by the rectifier 310 is regulated to a predetermined voltage by the regulator 340, and is supplied to the sequencer 360. On the other hand, a signal received by the antenna 301 is demodulated by the demodulator 350 and is supplied to the sequencer 360, where a predetermined process is performed thereon. The processed data is recorded in the memory 370.

**[0004]** Furthermore, when responding to the R/W device 200, in the non-contact IC card 300, the data processed by the sequencer 360 is modulated by the modulator 320, and the data is transmitted from the antenna

301. The signal received by the antenna 204 of the R/W device 200 is demodulated by the modem 201, and the signal is processed by the CPU 202.

**[0005]** In such a non-contact IC card system, IC chips used in a non-contact IC card are formed into one chip by MOS processing, a carrier wave from an external reader/writer is rectified to generate a DC voltage, and the voltage is used as power for the internal CPU and circuits such as the memory.

**[0006]** On the other hand, a mobile communication device having a so-called IC card function, such as a data carrying and processing function, by incorporating an integrated circuit in a mobile communication device has been proposed. However, specific realizing means have not been made clear.

### Disclosure of the Invention

**[0007]** The present invention has been made in view of the above-described problems. In a portable terminal device having an IC card function, a power supply obtained by rectifying a carrier wave and an external power supply are switched seamlessly, the power supply is installed in the portable terminal device, and a semiconductor integrated-circuit in which analog front-ends for both an IC card and an information processing apparatus such as a reader/writer are installed is realized.

**[0008]** The present invention provides a semiconductor integrated-circuit device comprising a semiconductor integrated-circuit device including rectifying means for rectifying a received carrier wave; generation means for generating a first power-supply voltage on the basis of an output by the rectifying means; and selection means for selecting one of the first power-supply voltage generated by the generation means and a supplied second power-supply voltage as a power-supply voltage required for operation, wherein the selection means selects the first power-supply voltage as a power-supply voltage required for operation when the second power-supply voltage is equal to or lower than a threshold value, and selects the second power-supply voltage as a power-supply voltage required for operation when the second power-supply voltage is equal to or higher than the threshold value and an instruction of operation in accordance with a predetermined function is given.

**[0009]** In a case where the second power-supply voltage is equal to or higher than a predetermined threshold value, the selection means may select the second power-supply voltage as a power-supply voltage required for operation when an instruction of operation in accordance with a data carrying and processing function for performing communication with an external information processing apparatus is given or when an instruction of operation in accordance with a function for performing communication with an external data carrying and processing medium is given.

**[0010]** A function section for realizing a data carrying and processing function, and a function section for per-

forming communication with an external data carrying and processing medium may be formed by one chip.

[0011] The semiconductor integrated-circuit device may further comprise control means, disposed between a connection point of the rectifying means and the generation means and a grounding point, for controlling the switching of a switch in accordance with a signal instructing an operation in accordance with a function for performing communication with an external data carrying and processing medium. The control means may turn off the switch when the signal is supplied, and may prevent an inflow of leakage electrical current by the generation means, which occurs when the second power-supply voltage is selected, by the selection means, as a power-supply voltage required for operation.

[0012] The semiconductor integrated-circuit device may further comprise clock extraction means for extracting a first clock on the basis of the carrier wave; clock generation means for generating a second clock; and clock selection means for selecting, as a clock used as an operation reference, one of the first clock extracted by the clock extraction means and the second clock generated by the clock generation means. The clock selection means may select the first clock as a clock used as an operation reference when the first clock is extracted by the clock extraction means, and may select the second clock as a clock used as an operation reference when the first clock is not extracted by the clock extraction means.

[0013] The present invention provides a portable terminal device comprising a semiconductor integrated-circuit device including: rectifying means for rectifying a received carrier wave; generation means for generating a first power-supply voltage on the basis of an output by the rectifying means; and selection means for selecting one of the first power-supply voltage generated by the generation means and a supplied second power-supply voltage as a power-supply voltage required for operation, wherein the selection means selects the first power-supply voltage as a power-supply voltage required for operation when the second power-supply voltage is equal to or lower than a threshold value, and selects the second power-supply voltage as a power-supply voltage required for operation when the second power-supply voltage is equal to or higher than the threshold value and an instruction of operation in accordance with a predetermined function is given.

[0014] The present invention provides a settlement method for performing a settlement with a portable terminal device comprising rectifying means for rectifying a received carrier wave; generation means for generating a first power-supply voltage on the basis of an output by the rectifying means; selection means for selecting one of the first power-supply voltage generated by the generation means and a supplied second power-supply voltage as a power-supply voltage required for operation; and storage means for storing money information as a predetermined transaction price, the settlement

method comprising a communication step of performing communication with the portable terminal device; a reading step of reading the money information stored in the portable terminal device in the process of the communication step; and a settlement step of performing a settlement on the basis of the money information read in the process of the reading step.

[0015] In the semiconductor integrated-circuit device of the present invention and in the semiconductor integrated-circuit device provided in a portable terminal device, a received carrier wave is rectified, a first power-supply voltage is generated on the basis of the output thereof, and one of the generated first power-supply voltage and the supplied second power-supply voltage is selected as a power-supply voltage required for operation. When the second power-supply voltage is equal to or lower than a threshold value, the first power-supply voltage is selected as a power-supply voltage required for operation. When the second power-supply voltage is equal to or higher than the threshold value and an instruction to operate in accordance with a predetermined function is given, the second power-supply voltage is selected as a power-supply voltage required for operation.

[0016] In the settlement method of the present invention, money information which is communicated with the portable terminal device and is stored in the portable terminal device is read, and a settlement is performed on the basis of the read money information.

#### Brief Description of the Drawings

#### [0017]

Fig. 1 is an overall diagram illustrating a ticket gate system in which a cellular phone of a first embodiment of the present invention is used as a non-contact card.

Fig. 2 is an overall diagram illustrating a ticket gate system in which a cellular phone of the first embodiment of the present invention is used as a reader/writer device.

Fig. 3 shows a configuration of a cellular phone having a reading/writing function of the first embodiment of the present invention.

Fig. 4 shows a flowchart for selecting one of three operation modes of the cellular phone of the first embodiment of the present invention.

Fig. 5 shows a carrier-wave rectification circuit and a logic circuit of the cellular phone of the first embodiment of the present invention.

Fig. 6 illustrates control logic of a switch SWen of the first embodiment of the present invention.

Fig. 7 illustrates control logic of a switch SWcont of the first embodiment of the present invention.

Fig. 8 shows a carrier-wave rectification circuit and a logic circuit of a cellular phone of a second embodiment of the present invention.



Fig. 9 illustrates control logic of a switch SW of the second embodiment of the present invention.

Fig. 10 shows an overall configuration of a conventional non-contact card and a reader/writer device for reading/writing the non-contact card.

#### Best Mode for Carrying Out the Invention

##### First Embodiment.

**[0018]** Fig. 1 is an overall diagram illustrating a ticket gate system in which a cellular phone of a first embodiment of the present invention is used as a non-contact card. In Fig. 1, reference numeral 102 denotes a cellular phone having a card operation function. Reference numeral 104 denotes a ticket gate provided in a station, etc. Reference numeral 106 denotes a reader/writer device having a reading/writing function, provided in the ticket gate 104. Reference numeral 108 denotes a closed ticket gate passing plate. Reference numeral 110 denotes an open ticket gate passing plate.

**[0019]** Next, the ticket gate system of Fig. 1 will be described briefly. A user, carrying the cellular phone 102, approaches the ticket gate 104. The reader/writer device 106 disposed in the ticket gate 104 detects that the cellular phone 102 of the user has reached the ticket gate 104 and performs communication with the cellular phone 102. When mutual authentication (for example, a settlement based on money information stored in the cellular phone 102) can be confirmed, the ticket gate passing plate 108 is driven, and the ticket gate passing plate 108 is opened as shown at reference numeral 110. This makes it possible for the user to pass through the ticket gate 104.

**[0020]** Fig. 2 is an overall diagram illustrating a settlement system in which a cellular phone of the first embodiment of the present invention is used as a reader/writer device. In Fig. 2, reference numeral 102 denotes a cellular phone having a reading/writing function. Reference numeral 120 denotes a non-contact IC card. Reference numeral 121 denotes a base-station antenna. Reference numeral 122 denotes a mobile communication network. Reference numeral 123 denotes a control computer provided in the central site, etc. Reference numeral 124 denotes a data memory connected to the control computer 123.

**[0021]** Next, a reading/writing operation performed by the cellular phone 102 with the non-contact IC card 120 in the settlement system of Fig. 2 will be described briefly. For example, when an amount of boarding points recorded in the non-contact IC card 120 becomes small, the cellular phone 102 reads point information recorded in the non-contact IC card 120 in order to recognize the amount of the shortage. When the points recorded in the non-contact IC card 120 are insufficient, the cellular phone 102 accesses the control computer 123 via the base-station antenna 121 and the mobile communication network 122, obtains points therefrom, and writes

the obtained points into the non-contact IC card 120 by using the reading/writing function.

**[0022]** Next, a description is given of a configuration of the cellular phone 102 of the first embodiment of the present invention having the above-described reading/writing function. Fig. 3 shows an example of an internal configuration of an IC provided inside the cellular phone 102 according to the present invention having a reading/writing function. In Fig. 3, an IC 300 is formed of a card function section 400, a reading/writing function section 500, and a control section 600.

**[0023]** In the card function section 400, a carrier wave received by an antenna 130 is rectified by a rectifier 131, and this wave is output to a power-supply control circuit 138 via a series regulator 133. The power-supply control circuit 138 controls a voltage from the series regulator 133 and supplies a power-supply voltage ( $V_{DD}$ ) used in the entire IC 300. Here, the series regulator 133 is a device for making the output voltage substantially uniform regardless of the input voltage. That is, the power-supply control circuit 138 performs an operation such as that described above by increasing the internal impedance when the input voltage is high and by decreasing the internal impedance when the input voltage is low. A voltage detector 140 monitors the voltage of an external power supply (battery) connected to the power-supply control circuit 138, and outputs a signal for prohibiting the use of the external power supply to the power-supply control circuit 138 when the voltage of the external power supply falls below a predetermined voltage. A signal received from the external reader/writer is input via the rectifier 131 to a receiver 139, where the signal is converted into a baseband signal, and this signal is passed to a signal processing unit (SPU) 144 and is processed thereby. Furthermore, a transmission signal to the external reader/writer is passed from the SPU 144 to the rectifier 131, where the value of a load connected to the antenna 130 is changed according to the transmission signal, and amplitude modulation is performed on a carrier wave generated by the external reader/writer.

**[0024]** In a carrier-wave detector 134, it is determined whether or not a carrier wave is contained in the radio wave received by the antenna 130. When it is determined by the carrier-wave detector 134 that a carrier wave is contained, a carrier-wave detection signal VR is output from the carrier-wave detector 134 to the power-supply control circuit 138. A clock extractor 135 extracts a clock from the input radio wave input from the antenna 130 and supplies the extracted clock to a clock selector 136. A clock oscillator 137 causes a crystal vibrator provided outside the IC chip to generate a clock of a frequency (e.g., 13.56 MHz) used in the IC 300 and supplies the clock to the clock selector 136. The clock selector 136 selects one of the clock supplied from the clock extractor 135 and the clock supplied from the clock oscillator 137, and supplies the selected clock to all sections of the IC 300. The entire operation of the card function section 400 is controlled by a CPU 145 of the control

section 600 (to be described later).

[0025] The reading/writing function section 500 comprises a transmission amplifier 150, a received-signal detector 153, and a receiving amplifier 154. The reading/writing function section 500 has a transmission and receiving function, causes the transmission amplifier 150 to amplify the transmission signal supplied from the SPU 144, and sends the resulting signal from an antenna 151. On the other hand, the signal received by an antenna 152 is detected by the received-signal detector 153 and is amplified by the receiving amplifier 154, and this signal is supplied to the SPU 144, where the signal is subjected to signal processing.

[0026] The control section 600 comprises a central processing unit (CPU) 145, a signal processing unit (SPU) 144, an encryption (DES) engine 146, a detector (CRC) 147 for detecting bit error, a primary memory (RAM) 141, a read-only memory (ROM) 142, an EEPROM 143 for recording data, a UART circuit 148 for performing asynchronous serial communication with an external source, and an I<sup>2</sup>C interface 149 for performing communication with the external source.

[0027] In the present invention, in the manner described above, since the card function section 400 and the reading/writing function section 500 are provided, the switching of the power supplies is controlled so that the most appropriate power supply is supplied to these sections. Furthermore, in the cellular phone, the antenna configuration used in the conventional IC card cannot be used as is, and an antenna in compliance with the cellular phone must be incorporated. Even in such a case, by supplying power to the IC card section (IC 300) from the battery, a sufficient power can be supplied.

[0028] The IC 300 has three operation modes: an external card mode in which communication with an external reader/writer device is performed via the card function section 400, a reader/writer mode in which communication with an external non-contact IC card is performed via the reading/writing function section 500, and an internal card mode in which an internal card disposed within is processed.

[0029] Fig. 4 shows a flowchart for selecting one of the above-described three operation modes. In Fig. 4, the CPU 145 performs mode determination in step S1. When the CPU 145 determines in step S1 that the mode is an external card mode, in step S5, the CPU 145 performs the external card mode which performs a card process with the external reader/writer device via the card function section 400. When the CPU 145 determines in step S1 that the mode is an internal card mode, in step S2, the CPU 145 performs command interpretation, and in the case of the internal card mode, a process for the card disposed within is performed. On the other hand, when the CPU 145 determines in step S2 that the mode is an R/W mode, the process proceeds to step S3, where a reader/writer mode is performed with an external non-contact IC card via the reading/writing function section 500.

[0030] The antennas for a card and for a reader/writer, or the transmission and receiving antennas for a reader/writer need not be separately provided, and one antenna may be used commonly.

[0031] In the present invention, the output (power generated from a carrier wave) of the power-supply control circuit 138 is supplied to a terminal  $V_{DD}$ . On the other hand, the power from the battery is also supplied to the terminal  $V_{DD}$  via the power-supply control circuit 138 in a similar manner. One of these two power supplies is selected according to a logical combination of a signal  $P_{ON}$  from the cellular phone (controller (not shown)) and a signal VR from the carrier-wave detector 134.

[0032] The switching of the power supplies of the cellular phone constructed as described above will be described below. Fig. 5 shows, in more detail, a carrier-wave rectification circuit and a logic circuit of the cellular phone of the first embodiment of the present invention. In Fig. 5, a carrier wave received by the antenna 130 is rectified by the rectifier 131, and this wave is supplied to the power-supply terminal  $V_{DD}$  via the series regulator 133.

[0033] On the other hand, the output of the battery 160 is connected to the power-supply terminal  $V_{DD}$  via the power-supply control circuit 138. This power-supply control circuit 138 is provided with a switch SWen 162 and a switch SWcont 164 between the input and the output. The switch SWen 162 is turned on/off in accordance with the control logic such as that shown in Fig. 6 by the power-supply voltage  $V_{BT}$  of the battery 160. On the other hand, the switch SWcont 164 is turned on/off in accordance with the control logic such as that shown in Fig. 7 by the signal  $P_{ON}$  supplied externally and the signal VR supplied from the carrier-wave detector 134.

[0034] Fig. 6 shows control logic for operating the switch SWen 162. As shown in Fig. 6, when the voltage  $V_{BT}$  of the battery 160 is equal to or higher than a threshold value, an enable signal en (signal indicating "1") is output from the power-supply detector 140 (VDET 140), and the switch SWen 162 is turned on by this signal. On the other hand, when the voltage  $V_{BT}$  of the battery 160 is lower than the threshold value, an enable signal en is not output from the power-supply detector 140 (VDET 140), and the switch SWen 162 is kept off.

[0035] Meanwhile, Fig. 7 shows control logic for operating the switch SWcont 164. As shown in Fig. 7, the switch SWcont 164 becomes on when either of the signal  $P_{ON}$  and the signal VR is high ("1"), otherwise, the switch SWcont 164 is kept off. Here, the signal  $P_{ON}$  is a reader/writer mode signal. When a user instructs the cellular phone to perform a reading/writing operation, a high signal  $P_{ON}$  is output from the cellular phone. On the other hand, regarding the Signal VR, when a carrier wave is detected within the received radio wave by the carrier-wave detector 134, a high signal VR is output. That is, the signal VR being high indicates that the cellular phone (IC 300) operates in the card mode, and the signal  $P_{ON}$  being high indicates that the cellular phone

operates in the reader/writer mode. The CPU 145 recognizes whether the cellular phone is operating in the card mode or in the reader/writer mode according to the signal VR or the signal P<sub>ON</sub>. In Fig. 7, when both the signal VR and the signal P<sub>ON</sub> are high, the control logic 163 exclusively selects one of the card mode and the reader/writer mode. This exclusive selection can be made in such a manner as to select in priority an operation of whichever becomes high earlier with respect to time. Of course, the selection may be made exclusively by another method.

[0036] The output of the switch SW<sub>cont</sub> 164 is connected to the output of the series regulator 133. Therefore, when both the switch SW<sub>en</sub> 162 and the switch SW<sub>cont</sub> 164 are on, the voltage V<sub>BT</sub> from the battery 160 is supplied as V<sub>DD</sub>. In the manner described above, the series regulator 133 is equivalently configured in such a manner as to control the internal impedance by the difference between the output voltage and a particular reference voltage. When the battery voltage is higher than the rectified voltage from the card (rectifier 131), generated by the series regulator 133, the internal impedance of the series regulator 133 becomes very large, and, thereby, the voltage of the battery 160 is prevented from flowing in reverse into the input side of the series regulator 133.

[0037] Even if a slight amount of electrical current flows in reverse into the input side of the series regulator 133 from the battery 160, the diode inside the rectifier 131 acts as a reverse bias, and the impedance is increased. Consequently, the reverse electrical current from the input side of the series regulator 133 to the rectifier 131 can be considerably decreased. On the other hand, when either of the switch SW<sub>en</sub> 162 and the switch SW<sub>cont</sub> 164 becomes off, the power from the battery 160 is not supplied as V<sub>DD</sub>, and only the rectified voltage of the carrier wave is supplied to the output of the series regulator 133, making it possible to seamlessly switch the power supplies without instantaneous power interruption. Furthermore, even when the distance to the external reader/writer device becomes relatively large and the power-supply voltage generated from the carrier wave cannot be sufficiently ensured, it becomes possible to perform communication by using power supplied from the battery 160. Therefore, it is possible to increase the distance in which communication is possible.

[0038] The circuits of Figs. 3 and 5 can be realized by MOS processing, and all the circuits in Figs. 3 and 5 can be disposed within a one-chip MOSLSI.

#### Second Embodiment.

[0039] Fig. 8 shows a cellular phone having a card function and a reading/writing function according to a second embodiment of the present invention. Fig. 8 shows a modified circuit of Fig. 5, which differs particularly from the first embodiment in that a power-supply

control circuit 170 and a protector 180 are provided.

[0040] A VDET 140 monitors the output voltage of the battery 160. When the output voltage (battery voltage V<sub>BT</sub>) is lower than a predetermined value, a signal VB of a "0" level is output to an AND circuit 171 of the power-supply control circuit 170, and when the battery voltage V<sub>BT</sub> is higher than the threshold value, a signal VB of a "1" level is output to the AND circuit 171.

[0041] The power-supply control circuit 170 controls a switch SW 173 in accordance with the signal VB supplied from the VDET 140, the signal P<sub>ON</sub> indicating operation in the reader/writer mode, and a VR signal supplied from the carrier-wave detector 134 when a carrier wave is detected within the received radio wave.

[0042] Fig. 9 shows control logic of the switch SW 173 by the power-supply control circuit 170. In Fig. 9, the signal V<sub>B</sub> corresponds to the output voltage of the VDET 140, becomes "0" when the battery voltage V<sub>BT</sub> is low, and becomes "1" when the battery voltage V<sub>BT</sub> is high. According to Fig. 9, the switch SW 173 is off while the battery voltage V<sub>BT</sub> is low (while the battery voltage V<sub>BT</sub> is equal to or lower than a threshold value), and is turned on when the battery voltage V<sub>BT</sub> becomes high (becomes equal to or higher than the threshold value) and when either of the signal P<sub>ON</sub> and the signal VR is high. The card mode, the reader/writer mode, and the exclusive logic are the same as in the case of Fig. 5. That is, when either of the signal P<sub>ON</sub> and the signal VR is high, the switch SW 173 is turned on.

[0043] The protector 180 is a circuit, formed by a MOS circuit, disposed on an IC chip. The protector 180 is connected between the connection point of the output of the rectifier 131 and the series regulator 133 and a ground, and is controlled in accordance with the reader/writer mode signal P<sub>ON</sub>. Specifically, when the reader/writer mode signal P<sub>ON</sub> is not applied (for example, when operating in the card mode), control is performed (the internal switch is turned on) so that the resistance of the protector 180 becomes very small, and an excessive voltage generated by the rectifier 131 in accordance with the carrier wave is output to the ground.

[0044] On the other hand, when the reader/writer mode signal P<sub>ON</sub> is applied, the resistance of the inside of the protector 180 is made to be infinitely large (the internal switch is turned off), thereby preventing the supply of the leakage current by the series regulator 133 to the internal resistance of the protector 180.

[0045] The protector 180 is provided in this manner, and the protector 180 is turned off during the reader/writer mode (when the reader/writer mode signal P<sub>ON</sub> is applied), making it possible to prevent loss of electrical current due to the leakage current of the series regulator 133, generated at a normal time (in particular, when realized by CMOS).

[0046] As a result, it is possible to form both the IC card and the reader/writer into one chip by MOS processing and possible to provide a cellular phone which realizes a low cost and high reliability of the prod-



uct.

[0047] The above-mentioned "IC card" and "card function" are used for the sake of convenience in order to indicate a medium having a data carrying and processing function, respectively, and does not indicate the card shape. Although in the above-described embodiments, a case in which a semiconductor integrated-circuit device is incorporated within the cellular phone is described as an example, the present invention can be applied to a fixed phone connected by wire, a portable information terminal (PDA), which is a small information device, a watch, a computer, etc., regardless of the form thereof and the presence or absence of a wire/wireless communication function. Furthermore, the semiconductor integrated-circuit device may be incorporated within the portable terminal device in either a removable or an irremovable form, and an external storage medium, such as an IC card incorporating an IC or a memory card, may be formed in such a manner as to be removable from a cellular phone, a portable information terminal, a computer, etc.

#### Industrial Applicability

[0048] As has thus been described, according to the present invention, since a semiconductor integrated-circuit capable of seamlessly switching between an external power supply and a carrier-wave rectified power supply can be realized, by incorporating the semiconductor integrated-circuit into various devices, it is possible to realize a device incorporating analog front-ends for both a card and an information processing apparatus such as a reader/writer. Furthermore, it becomes possible to use two communication interfaces of a wireless interface and a UART interface for the purpose of communication between a service memory compatible with a memory built in the semiconductor integrated-circuit and an external device. As a result, it becomes possible for the portable terminal device incorporating the semiconductor integrated-circuit in accordance with the present invention to operate as a remote IC card and to operate as a reader/writer for the remote IC card.

#### Claims

1. A semiconductor integrated-circuit device comprising:

rectifying means for rectifying a received carrier wave;  
generation means for generating a first power-supply voltage on the basis of an output by said rectifying means; and  
selection means for selecting one of said first power-supply voltage generated by said generation means and a supplied second power-sup-

ply voltage as a power-supply voltage required for operation,

wherein said selection means selects said first power-supply voltage as a power-supply voltage required for operation when said second power-supply voltage is equal to or lower than a threshold value, and selects said second power-supply voltage as a power-supply voltage required for operation when said second power-supply voltage is equal to or higher than the threshold value and an instruction to operate in accordance with a predetermined function is given.

2. A semiconductor integrated-circuit device according to Claim 1, wherein, in a case where said second power-supply voltage is equal to or higher than the predetermined threshold value, said selection means selects said second power-supply voltage as a power-supply voltage required for operation when an instruction to operate in accordance with a data carrying and processing function for performing communication with an external information processing apparatus is given or when an instruction to operate in accordance with a function for performing communication with an external data carrying and processing medium is given.

3. A semiconductor integrated-circuit device according to Claim 2, wherein a function section for realizing a data carrying and processing function, and a function section for performing communication with an external data carrying and processing medium are formed by one chip.

4. A semiconductor integrated-circuit device according to Claim 2, further comprising control means, disposed between a connection point of said rectifying means and said generation means and a grounding point, for controlling the switching of a switch in accordance with a signal instructing an operation in accordance with a function for performing communication with an external data carrying and processing medium,

wherein said control means turns off said switch when said signal is supplied, and prevents an inflow of leakage current by said generation means, which occurs when said second power-supply voltage is selected, by said selection means, as a power-supply voltage required for operation.

5. A semiconductor integrated-circuit device according to Claim 1, further comprising:

clock extraction means for extracting a first clock on the basis of said carrier wave;  
clock generation means for generating a second clock; and

clock selection means for selecting, as a clock used as an operation reference, one of said first clock extracted by said clock extraction means and said second clock generated by said clock generation means,

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wherein said clock selection means selects said first clock as a clock used as an operation reference when said first clock is extracted by said clock extraction means, and selects said second clock as a clock used as an operation reference when said first clock is not extracted by said clock extraction means.

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6. A portable terminal device comprising a semiconductor integrated-circuit device including:

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rectifying means for rectifying a received carrier wave;

generation means for generating a first power-supply voltage on the basis of an output by said rectifying means; and

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selection means for selecting one of said first power-supply voltage generated by said generation means and a supplied second power-supply voltage as a power-supply voltage required for operation,

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wherein said selection means selects said first power-supply voltage as a power-supply voltage required for operation when said second power-supply voltage is equal to or lower than a threshold value, and selects said second power-supply voltage as a power-supply voltage required for operation when said second power-supply voltage is equal to or higher than the threshold value and an instruction to operate in accordance with a predetermined function is given.

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7. A settlement method for performing a settlement, which occurs as a result of a predetermined transaction, with a portable terminal device comprising: rectifying means for rectifying a received carrier wave; generation means for generating a first power-supply voltage on the basis of an output by said rectifying means; selection means for selecting one of said first power-supply voltage generated by said generation means and a supplied second power-supply voltage as a power-supply voltage required for operation; and storage means for storing money information as a predetermined transaction price, said settlement method comprising:

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a communication step of performing communication with said portable terminal device;

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a reading step of reading said money information stored in said portable terminal device in the process of said communication step; and

a settlement step of performing a settlement on the basis of said money information read in the process of said reading step.



FIG. 1

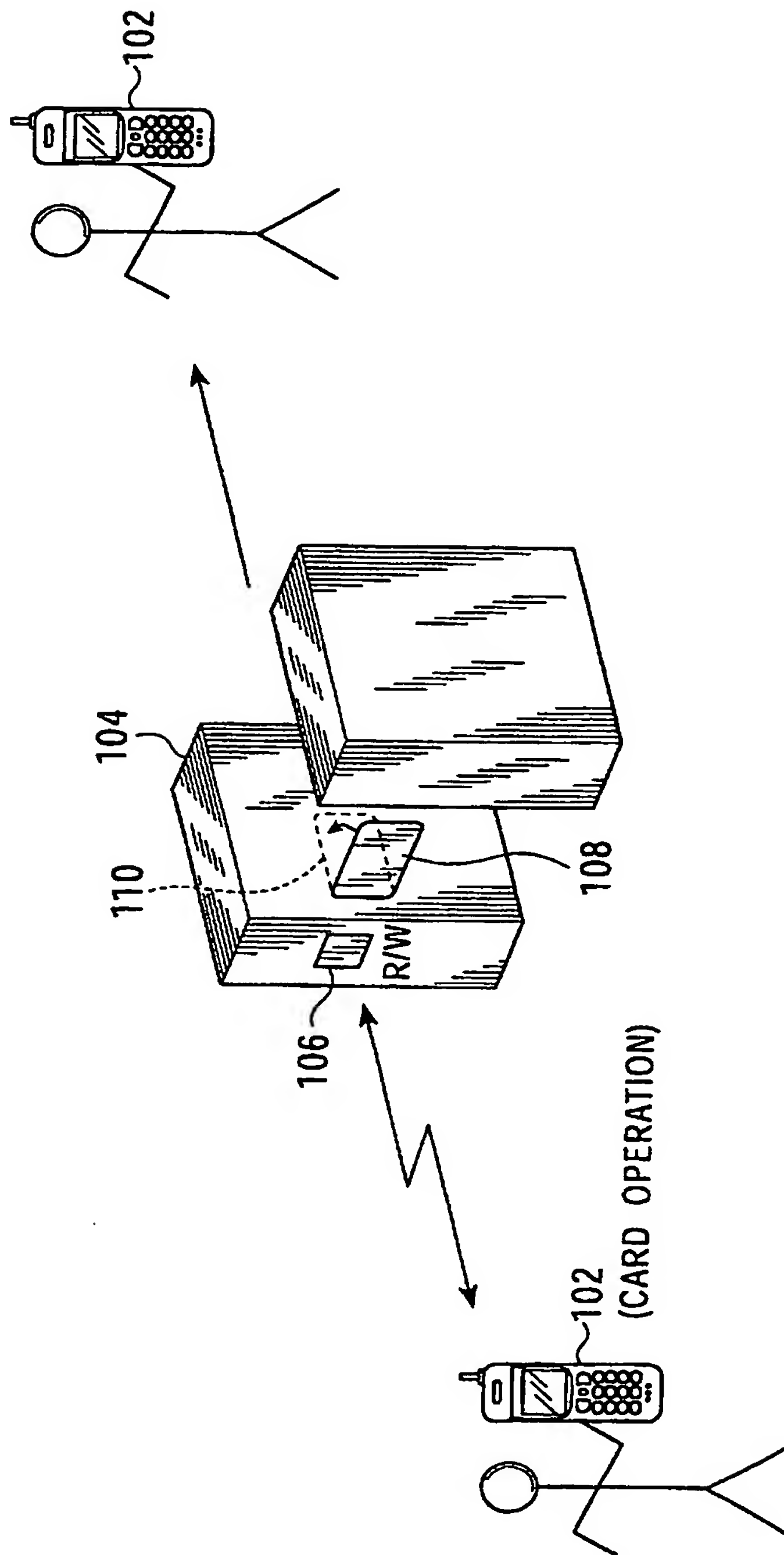


FIG. 2

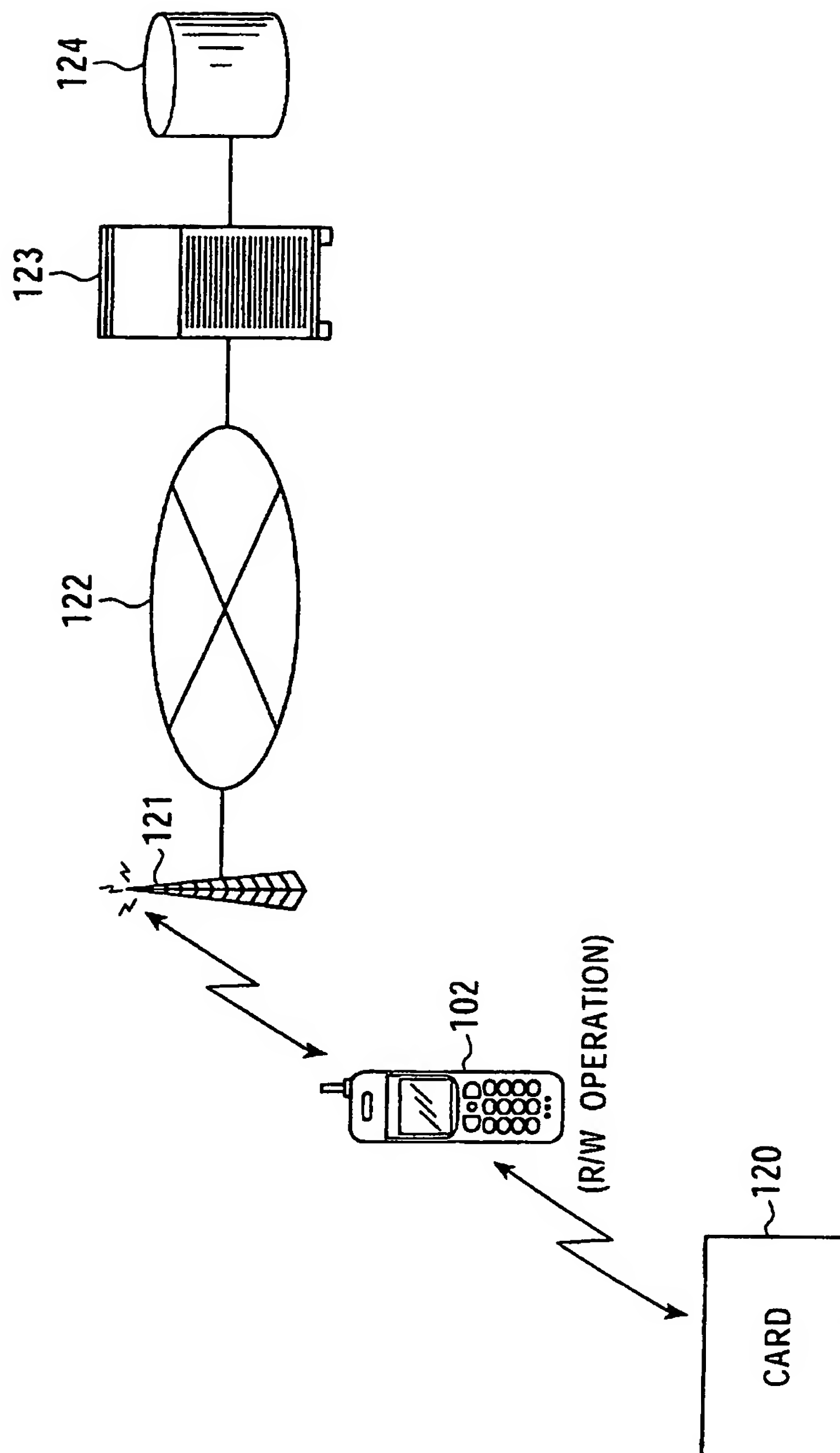


FIG. 3

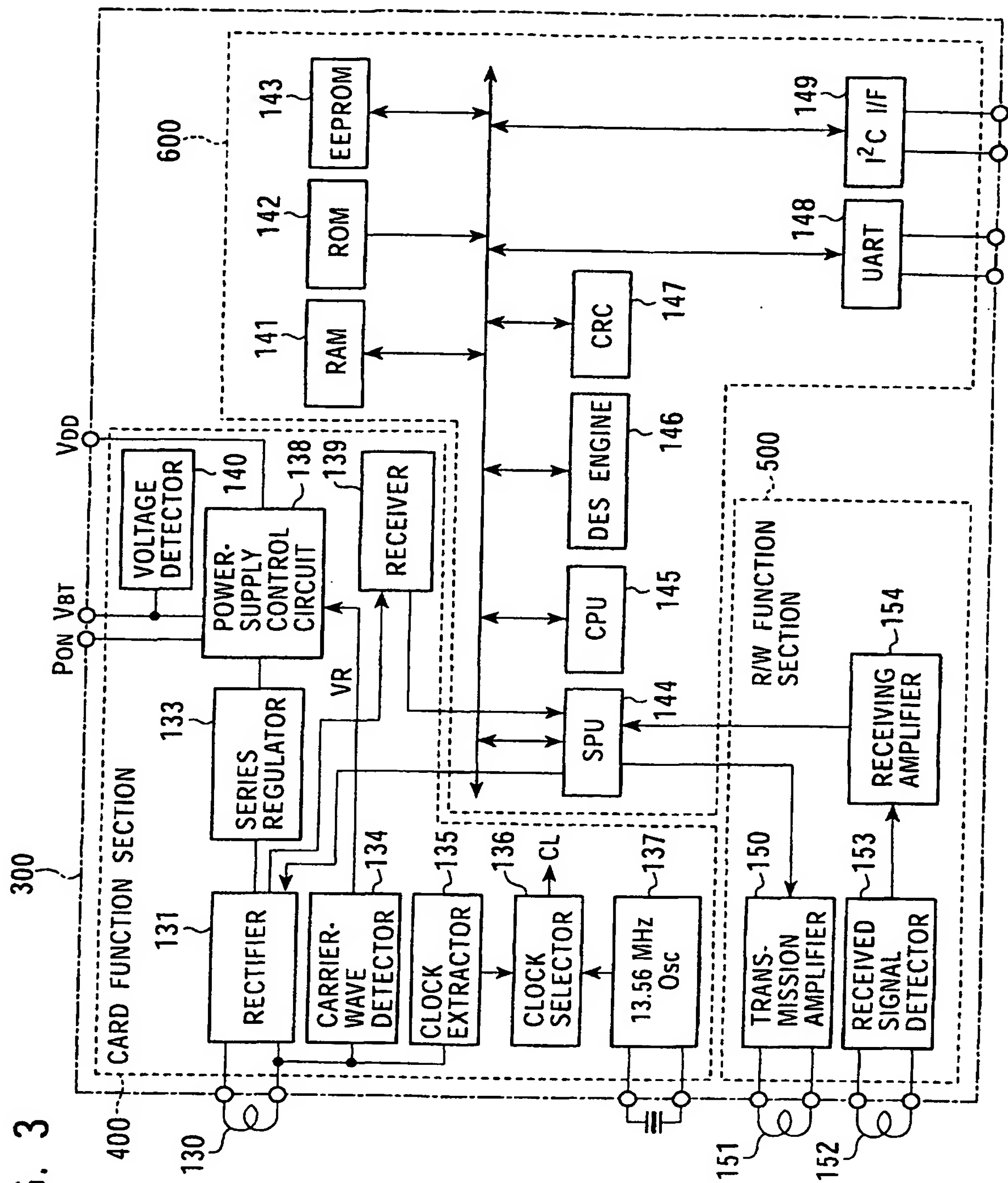




FIG. 4

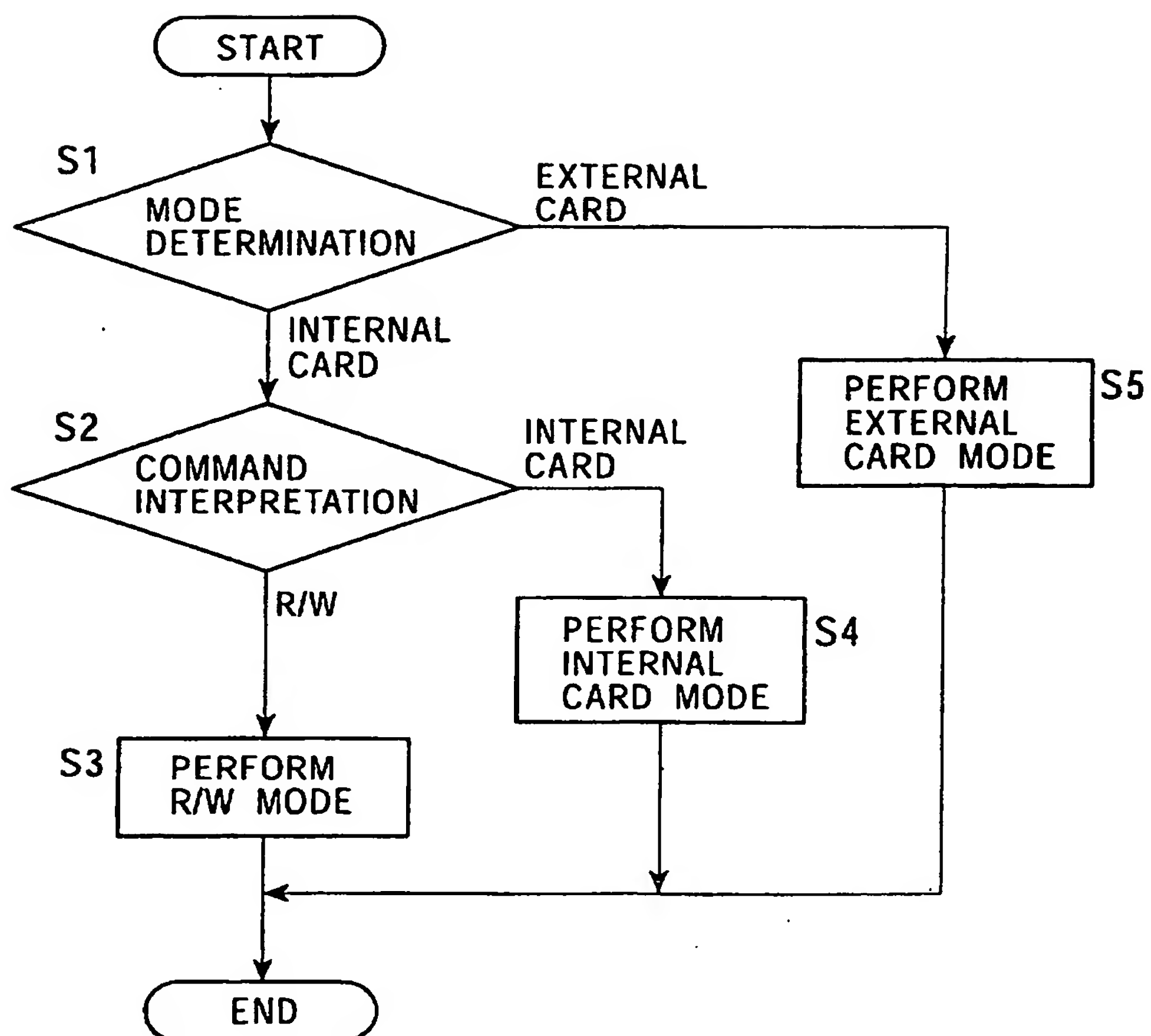


FIG. 5

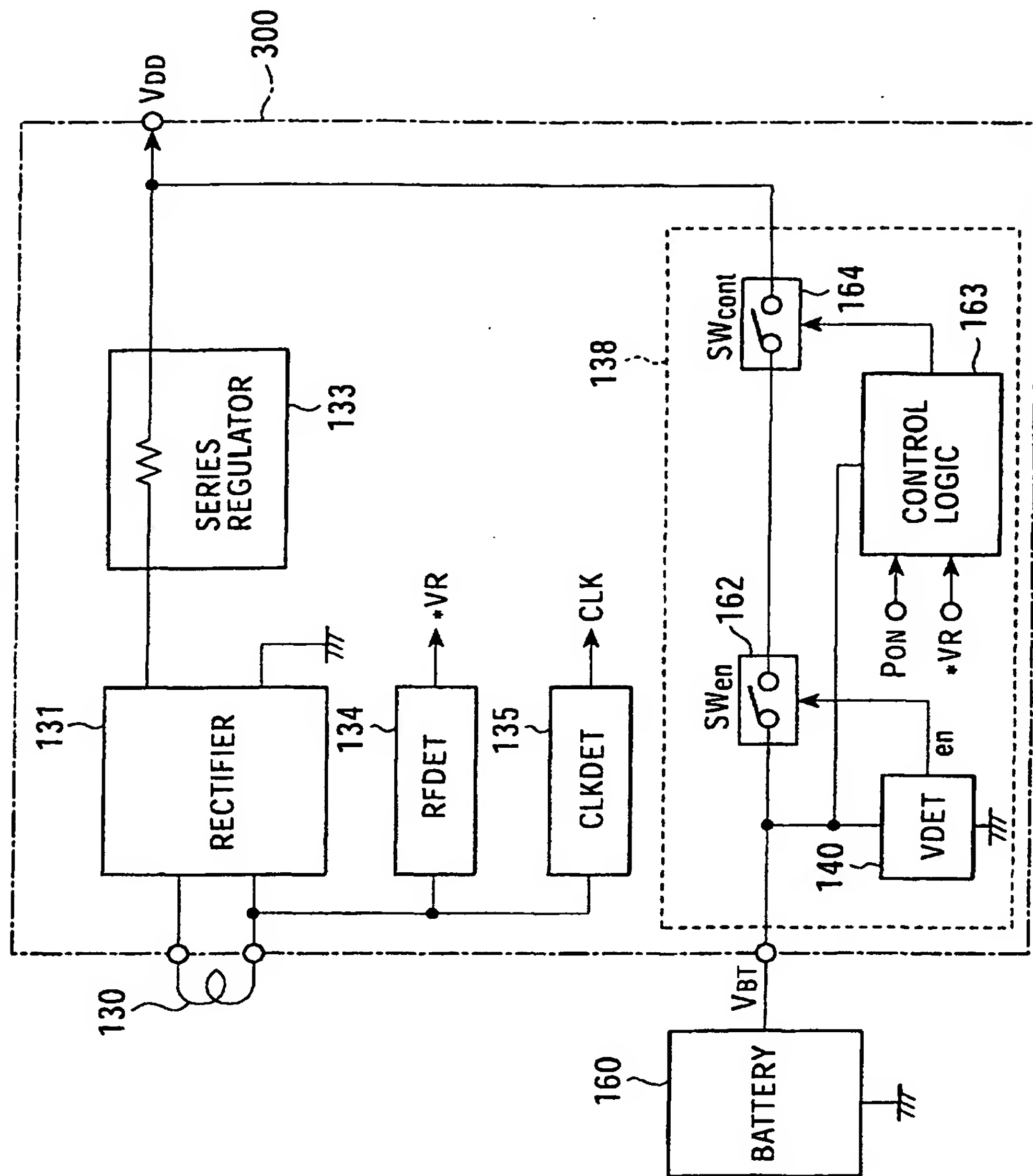


FIG. 6

BATTERY	en	SW <sub>en</sub>
PRESENT	1	ON
ABSENT	0	OFF



FIG. 7

Pon	VR	SW <sub>cont</sub>	OPERATION OF CPU SYSTEM
0	0	OFF	DOES NOT OPERATE
0	1	ON	CARD MODE
1	0	ON	R/W MODE
1	1	ON	EXCLUSIVELY SELECT CARD MODE OR R/W MODE

FIG. 8

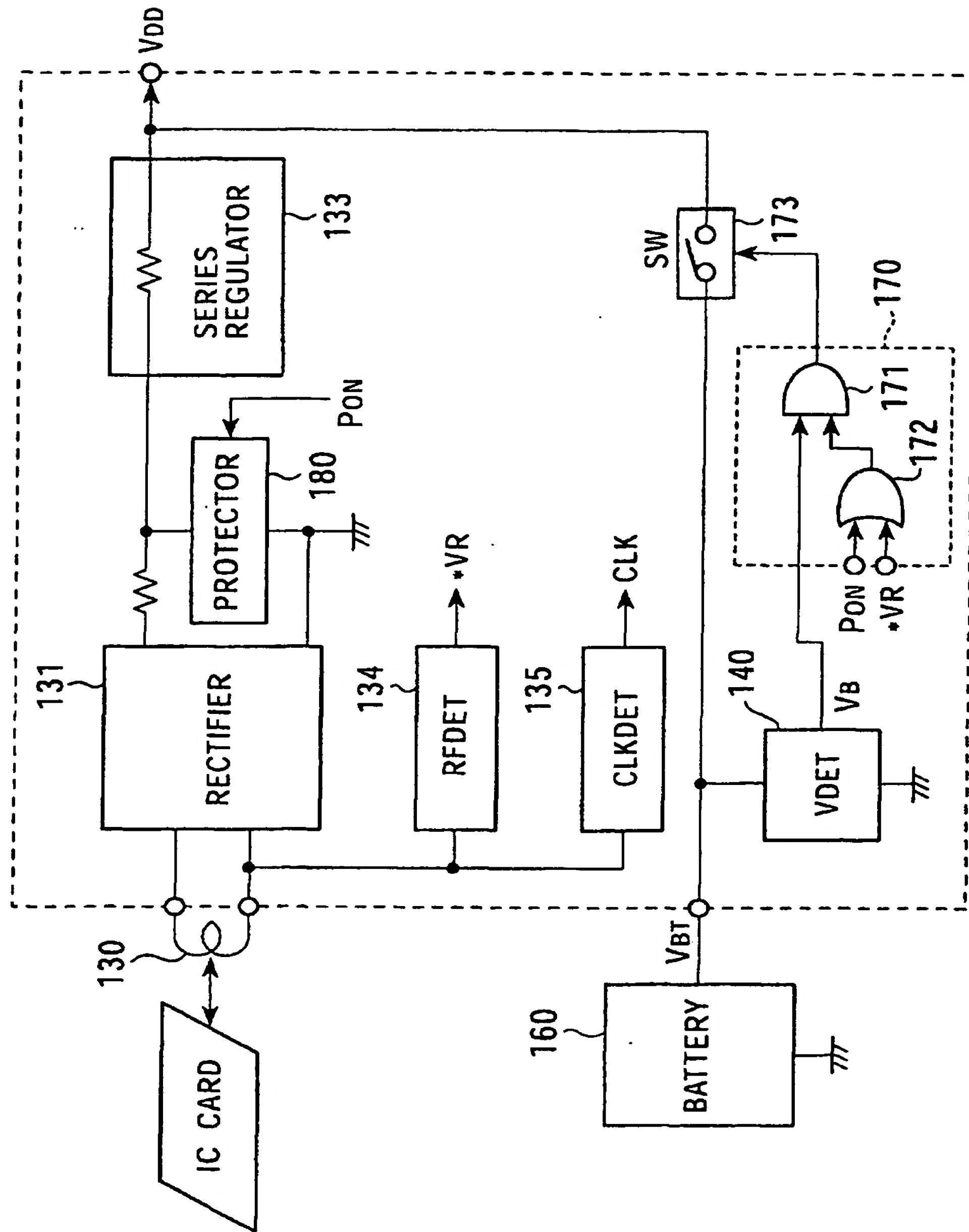
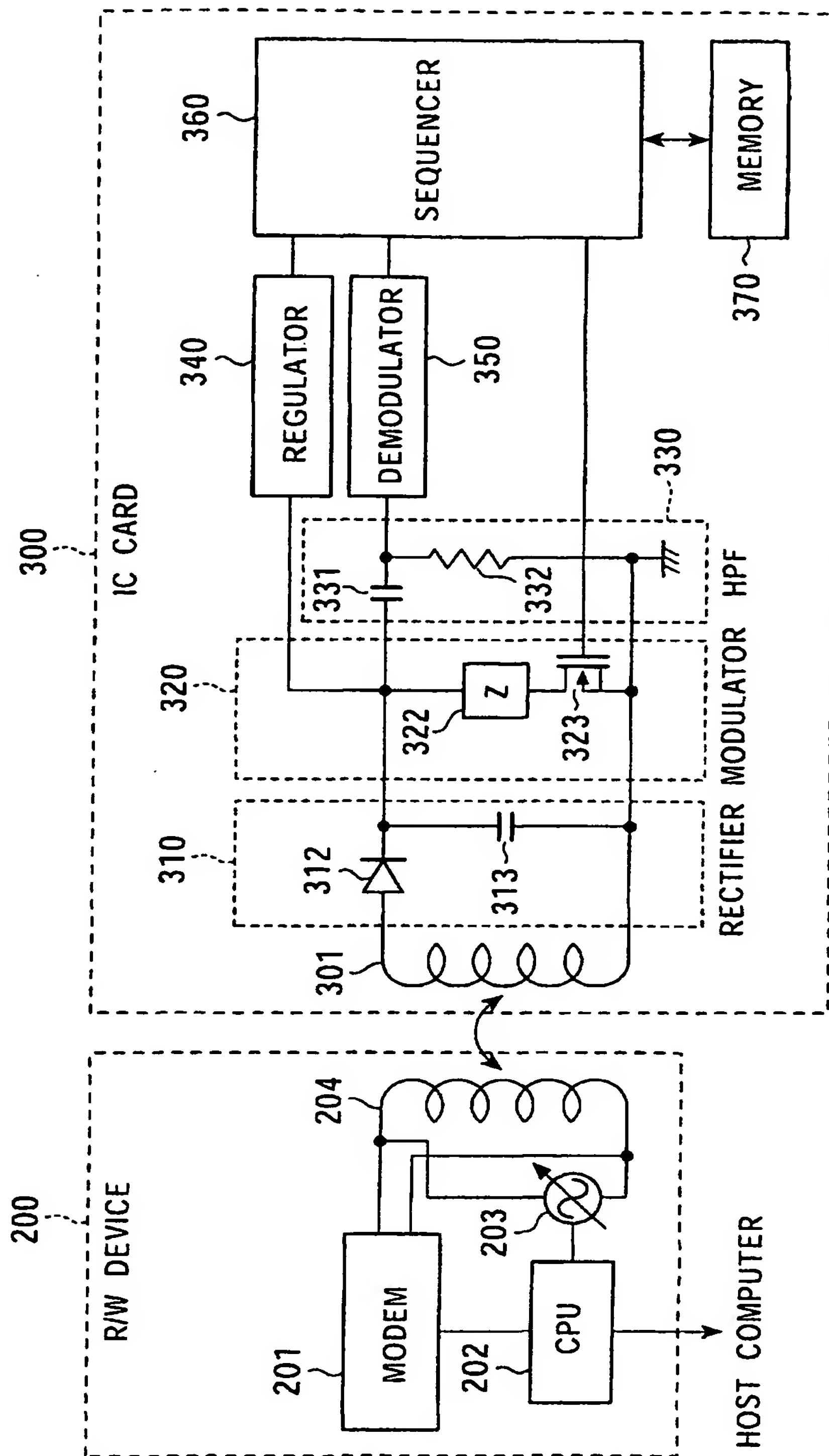


FIG. 9

V <sub>B</sub>	P <sub>ON</sub>	V <sub>R</sub>	S <sub>W</sub>	OPERATION OF CPU SYSTEM
0	0	0	OFF	DOES NOT OPERATE
0	0	1	OFF	DOES NOT OPERATE
0	1	0	OFF	DOES NOT OPERATE
0	1	1	OFF	DOES NOT OPERATE
1	0	0	OFF	DOES NOT OPERATE
1	0	1	ON	CARD MODE
1	1	0	ON	R/W MODE
1	1	1	ON	EXCLUSIVELY SELECT CARD MODE OR R/W MODE



FIG. 10



## INTERNATIONAL SEARCH REPORT

International application No.

PCT/JP02/01945

<b>A. CLASSIFICATION OF SUBJECT MATTER</b> Int.Cl <sup>7</sup> G06K17/00, H02J7/34, H02J17/00  According to International Patent Classification (IPC) or to both national classification and IPC		
<b>B. FIELDS SEARCHED</b> Minimum documentation searched (classification system followed by classification symbols) Int.Cl <sup>7</sup> G06F1/26-1/32, G06K17/00, G06K19/00-19/18, G07B15/00-15/04, H02J7/34-7/35, H02J17/00, H04B7/24-7/26, H04M11/00-11/10  Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched Jitsuyo Shinan Koho 1926-1996 Toroku Jitsuyo Shinan Koho 1994-2002 Kokai Jitsuyo Shinan Koho 1971-2002 Jitsuyo Shinan Toroku Koho 1996-2002  Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)		
<b>C. DOCUMENTS CONSIDERED TO BE RELEVANT</b>		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	JP 10-261053 A (Toshiba Corp.), 29 September, 1998 (29.09.98), Whole document (Family: none)	1-7
Y	JP 2000-184587 A (Sony Corp.), 30 June, 2000 (30.06.00), Whole document & CN 1260645 A	1-7
Y A	EP 820178 A (Motorola Inc.), 21 January, 1998 (21.01.98), Whole document & JP 10-98542 A & US 5943624 A	6-7 1-5
<input checked="" type="checkbox"/> Further documents are listed in the continuation of Box C. <input type="checkbox"/> See patent family annex.		
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Date of the actual completion of the international search 01 May, 2002 (01.05.02)		Date of mailing of the international search report 21 May, 2002 (21.05.02)
Name and mailing address of the ISA/ Japanese Patent Office		Authorized officer
Facsimile No.		Telephone No.

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## C (Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT

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Y A	WO 99/41714 A (Angewandte Digital Elektronik GMBH), 19 August, 1999 (19.08.99), Whole document & EP 1055207 A                      & JP 2002-503902 A	6-7 1-5
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A	JP 6-61993 A (Sony Corp.), 04 March, 1994 (04.03.94), Whole document (Family: none)	5

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